Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	(power ADJ adapter$1) and (signal ADJ adapter$1) and (pcb or (printed ADJ circuit ADJ board$1) or (circuit ADJ board$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:27
L2	47763	sata or usb	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 10:07
L3	173	(sata ADJ socket$1) or (usb ADJ socket$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 10:08
L4	5508	(pcb or (printed ADJ circuit ADJ board$1) or (circuit ADJ board$1)) and (mother ADJ board$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 11:05
L5	2102	4 and signal and power	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 11:06
L6	235	5 and adapter$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 11:07
L7	69	6 and socket$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 11:07
L8	0	(connector$1 NEAR (power ADJ socket$1)) and (connector$1 NEAR (signal ADJ socket$1))	USPAT	OR	OFF	2005/09/14 11:47

L10	0	(connector$1 NEAR (power ADJ socket$1)) and (connector$1 NEAR (signal ADJ socket$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 11:47
L14	1139	439/79.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:27
L15	79	439/947.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:27
L16	888	439/65.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:27
L17	973	439/76.1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:28
L18	971	439/638.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:28
L19	171	439/639.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/14 14:28